

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

AFIFW

m re Applicant:

Brian Taggart et al.

Art Unit:

2818

Serial No.:

10/807,830

Examiner:

Calvin Lee

Filed:

March 24, 2004

Docket:

ITL.1119US P18791

For:

Lower Profile Flexible Substrate

Package for Electronic Components

Assignee:

Intel Corporation

Mail Stop AF Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

REPLY TO FINAL REJECTION

Sir:

In response to the final rejection mailed June 24, 2005, please amend the abovereferenced patent application as follows:

K TO FWFTA

Date of Deposit: <u>July 6, 2005</u>
I hereby certify under 37 CFR 1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated above and is addressed to the Commissioner for Patents, P.O. Box 1450,

Cynthia L/ Hayden